

Figure 1

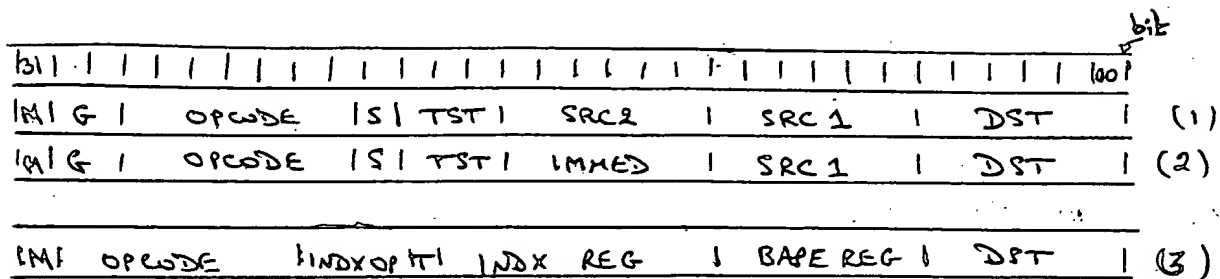


Figure 3

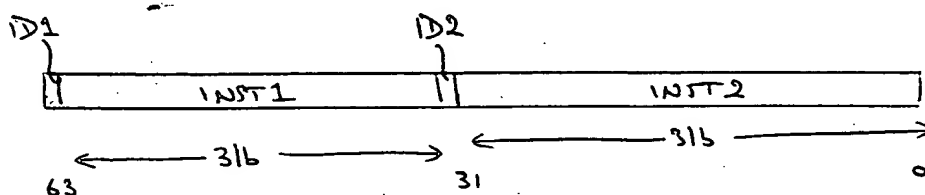


Figure 2

Treg byte

7	6	5	4	3	2	1	0
S				Test code			

Figure 4

CCY7	CCY6	CCY5	CCY4	CCY3	CCY2	CCY1	CCY0	CCX7	CCX6	CCX5	CCX4	CCX3	CCX2	CCX1	CCX0
------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------

N	Z	C	V
3	2	1	0

Figure 5

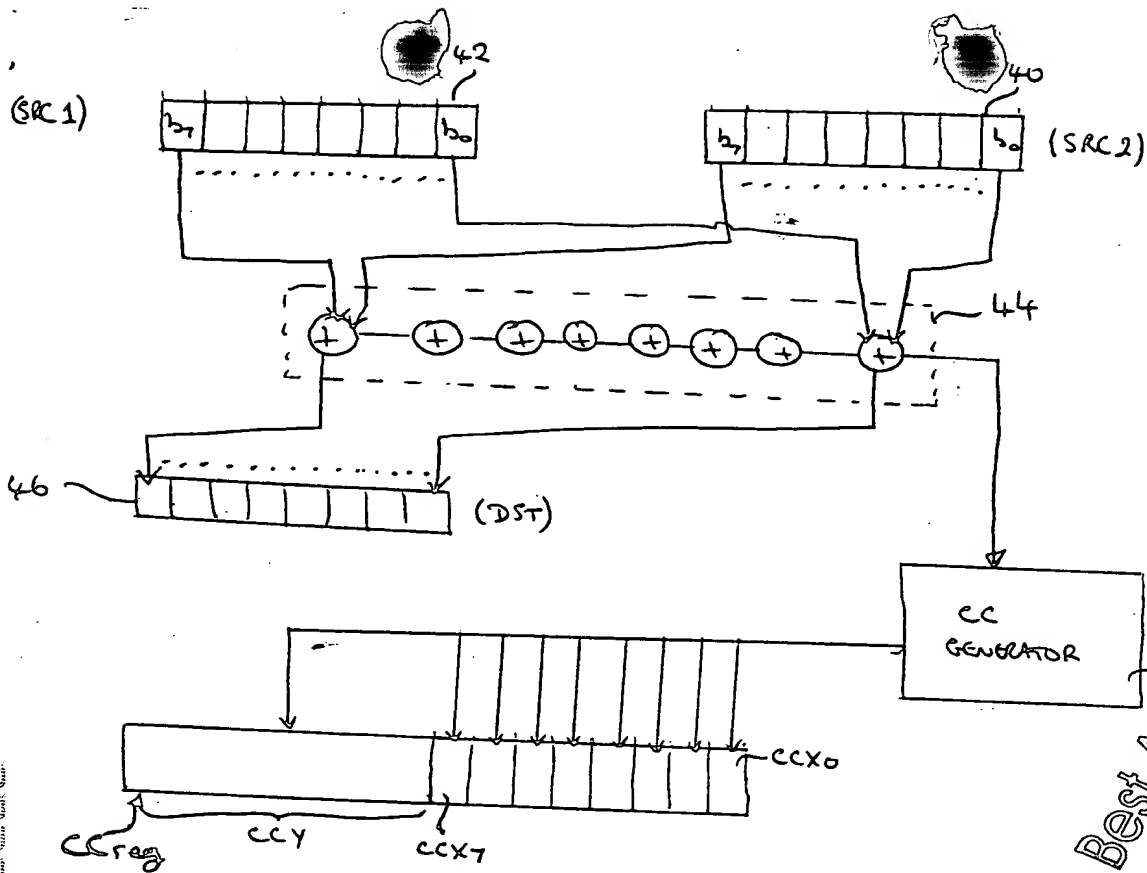


Figure 6

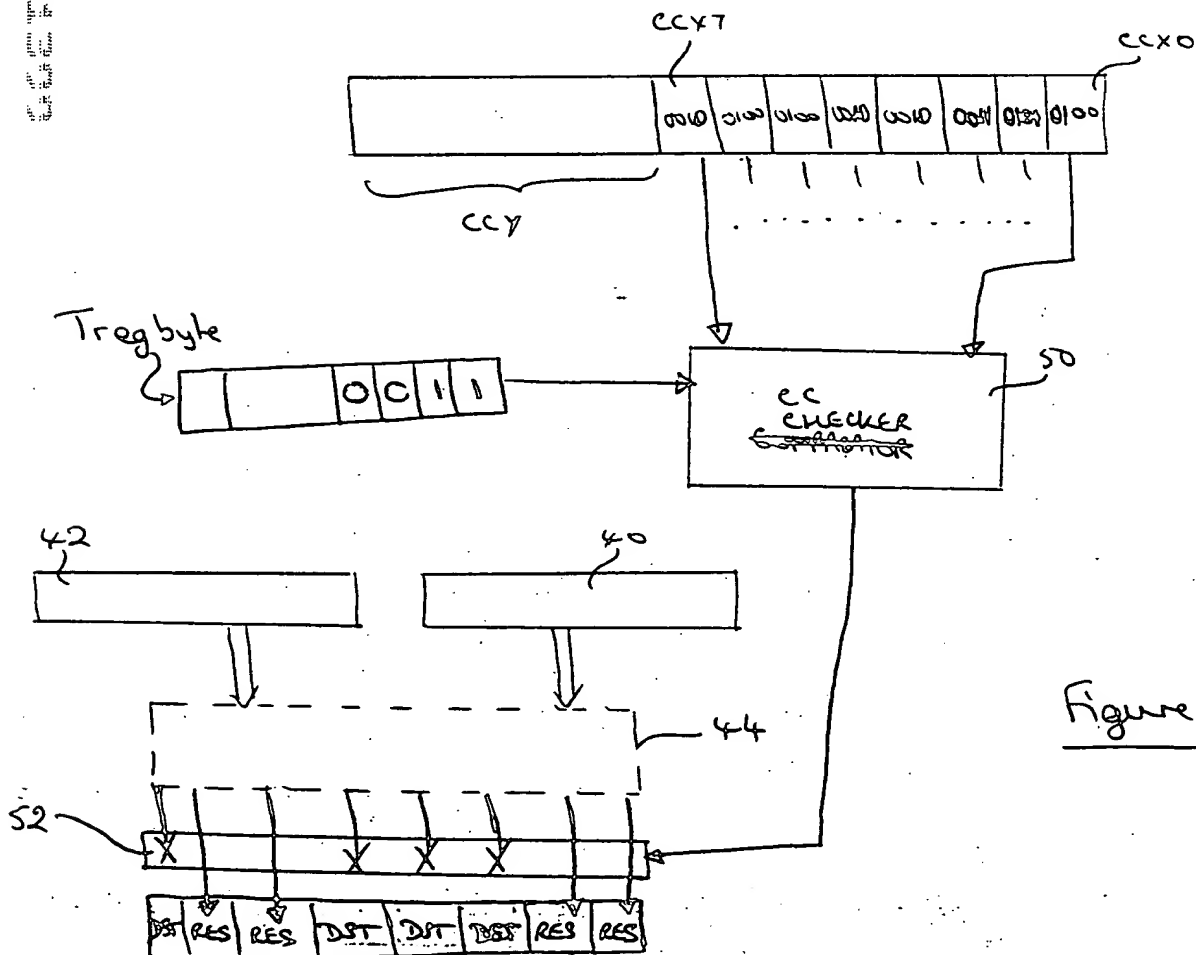


Figure 7